

AMENDMENTS TO THE CLAIMS

Please cancel claims 18-21, 30, 31, 33, 34, 39 and 40 and amend claims 32 and 35 as shown in the following listing of claims. This listing of claims will replace all previous versions and listings of claims in the application.

Claims 1-31. (cancelled)

32. (amended) A method of compensating for phase differences between a plurality of signals associated with a plurality of signal levels, comprising steps of:

- (a) comparing a rising edge of a first signal associated with a first signal level with a rising edge of a second signal associated with a second signal level;
- (b) generating a rising edge compensation signal indicative of a phase difference between the rising edge of the first signal and the rising edge of the second signal;
- (c) comparing a falling edge of the first signal with a falling edge of the second signal;
- (d) generating a falling edge compensation signal indicative of a phase difference between the falling edge of the first signal and the falling edge of the second signal;
- (e) delaying the rising edge of the first signal in response to the rising edge compensation signal; and
- (f) delaying the falling edge of the first signal in response to the falling edge compensation signal;

wherein delaying step (e) comprises buffering the first signal with a buffer transistor and adjusting current flow through the buffer transistor, thereby controlling delay of the rising edge of the first signal through the buffer transistor.

33-34. (cancelled)

35. (amended) The method of claim 34 32 wherein delaying step (f) comprises adjusting current flow through the buffer transistor, thereby controlling delay of the falling edge of the first signal through the buffer transistor.

36. (previously presented) The method of claim 35 wherein generating step (b) comprises generating a rising edge compensation signal indicative of whether the rising edge of the first signal leads the rising edge of the second signal.

37. (previously presented) The method of claim 36 wherein generating step (d) comprises generating a falling edge compensation signal indicative of whether the falling edge of the first signal leads the falling edge of the second signal.

38. (previously presented) The method of claim 37 further comprising a step (h) of:
(h) filtering the rising edge compensation signal and the falling edge compensation signal with low-pass filters.

39-40. (cancelled)